

FIG. 1

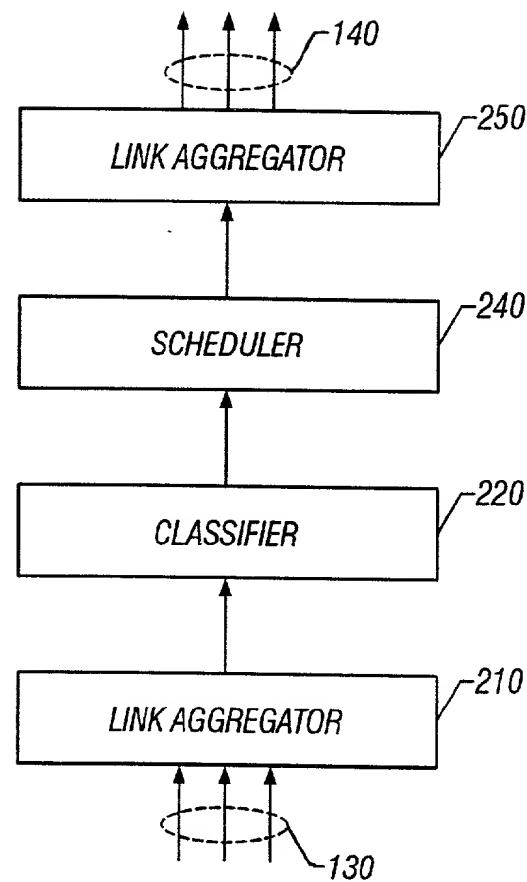


FIG. 2

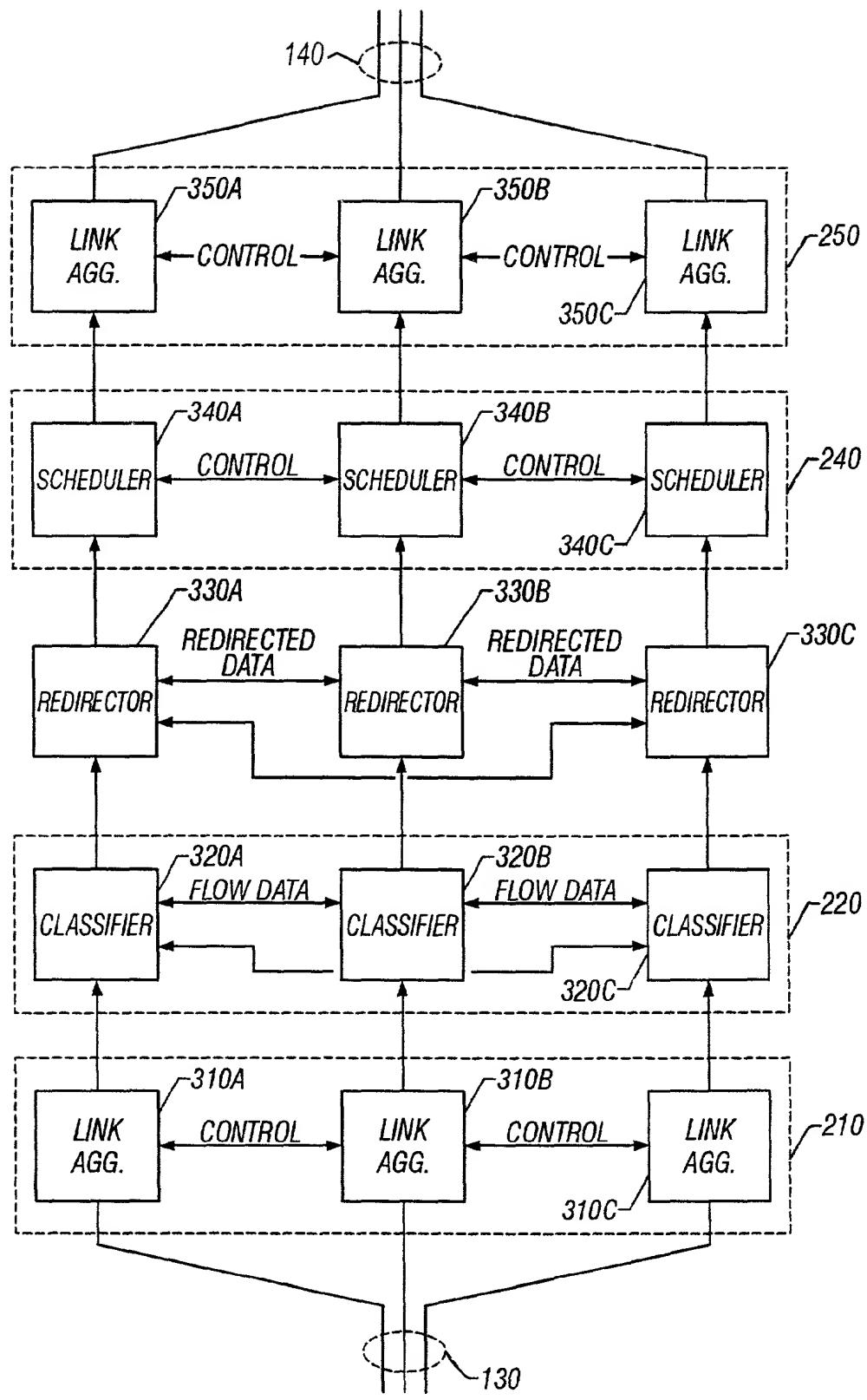


FIG. 3

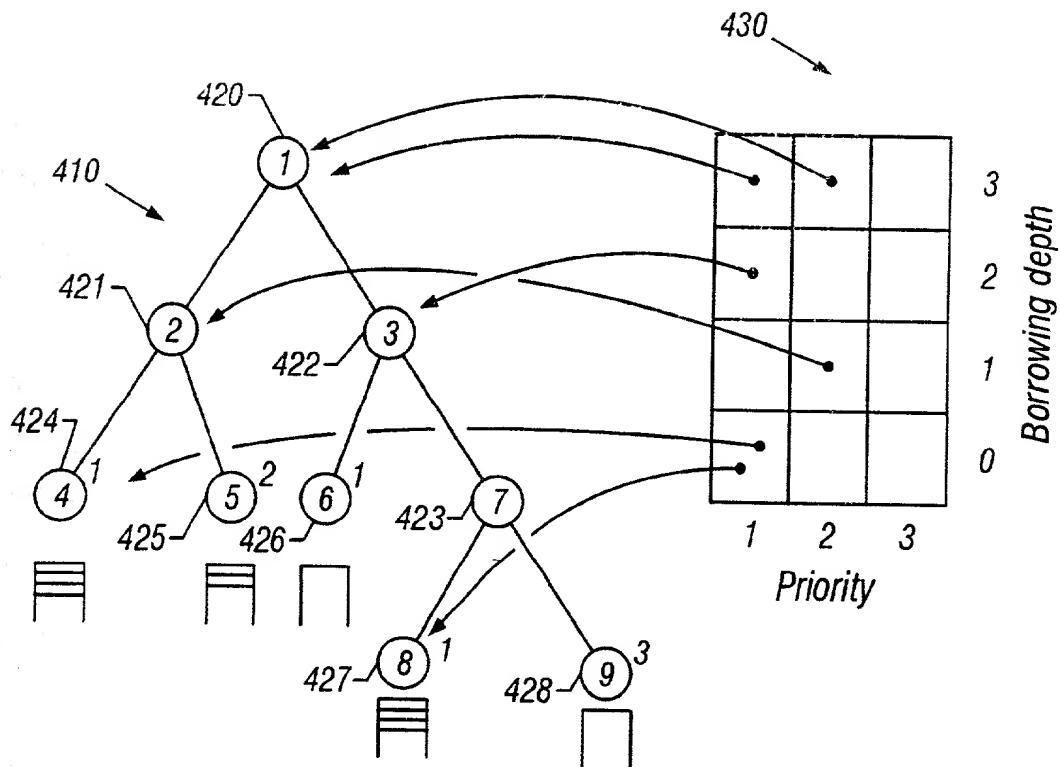


FIG. 4A

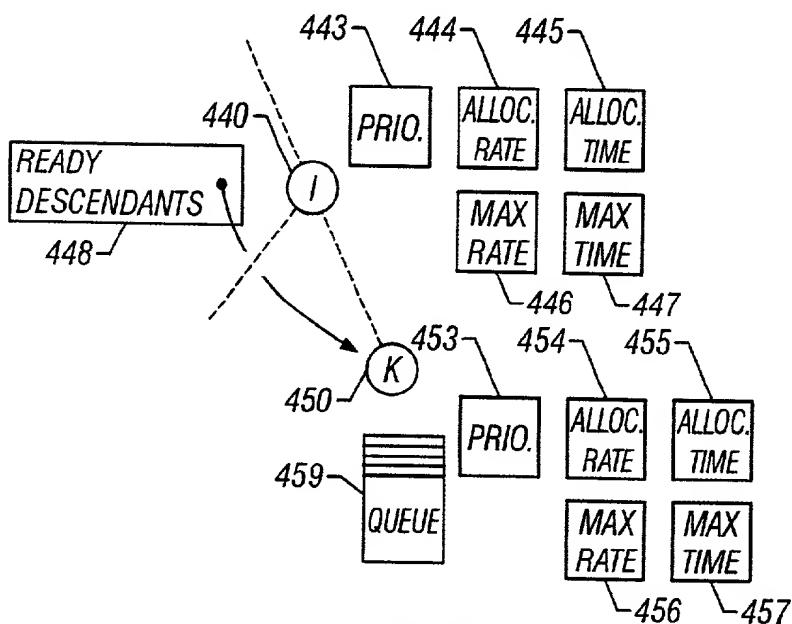


FIG. 4B

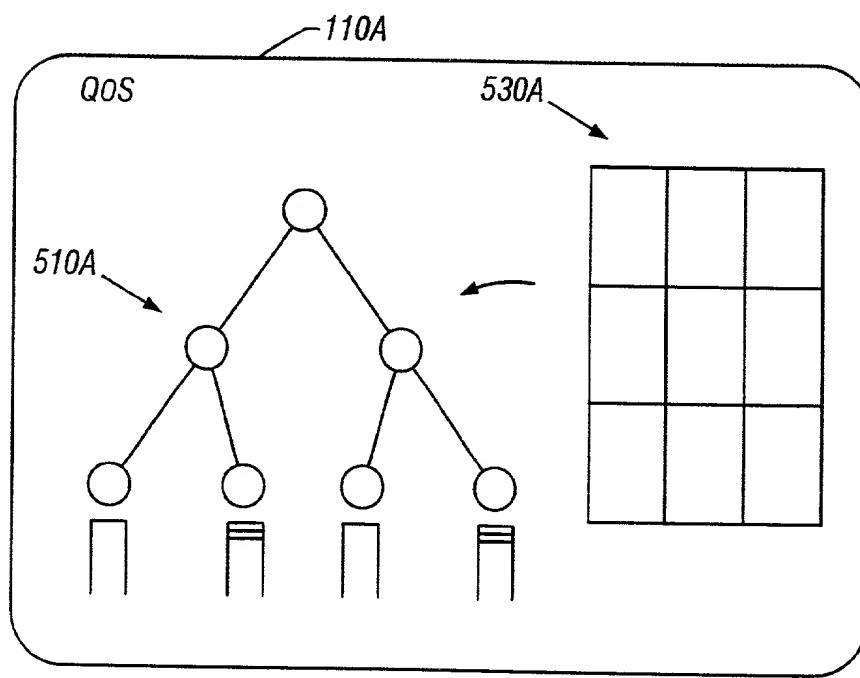
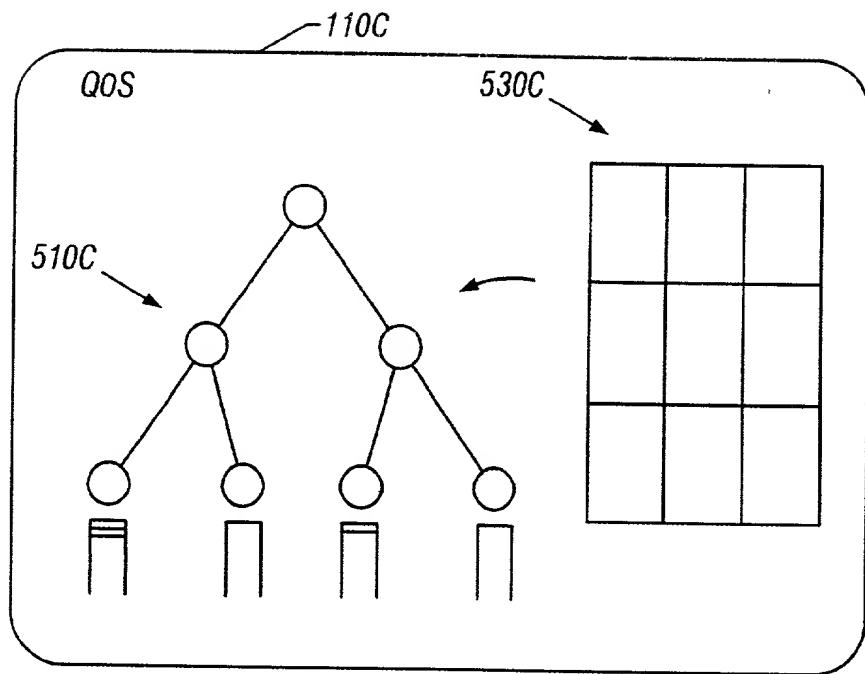


FIG. 5

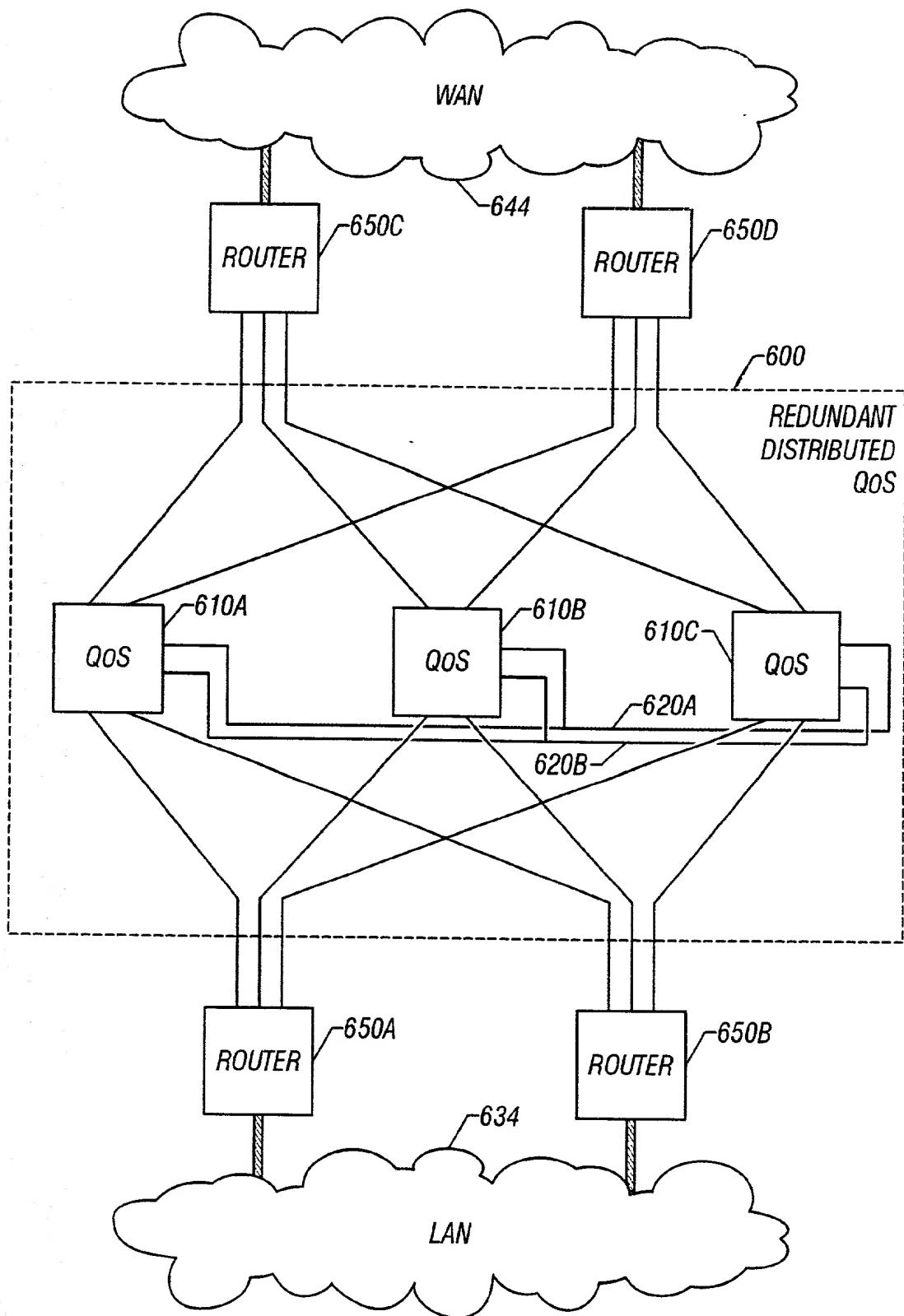


FIG. 6

110

QOS PROCESSOR

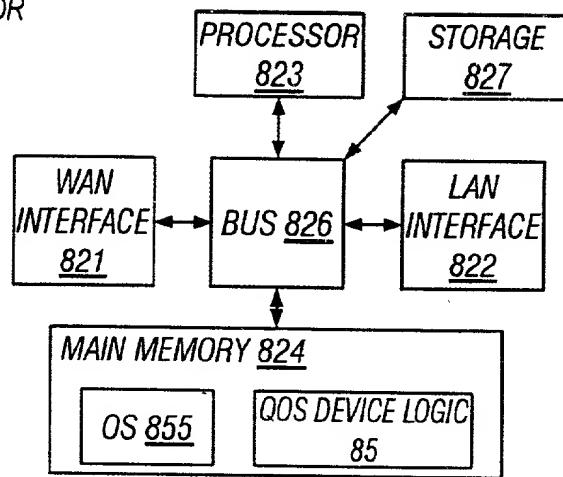


FIG. 7A

85

QoS DEVICE LOGIC

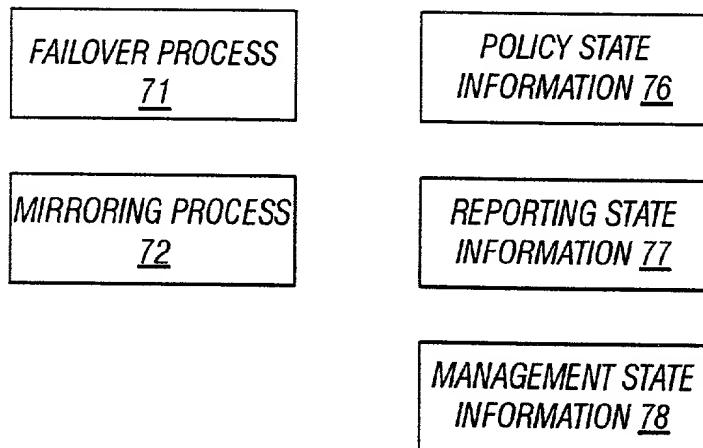


FIG. 7B

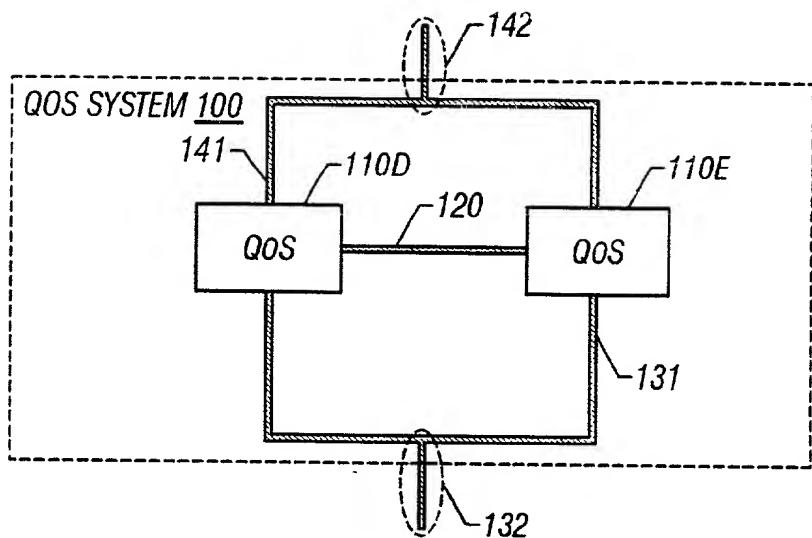


FIG. 8A

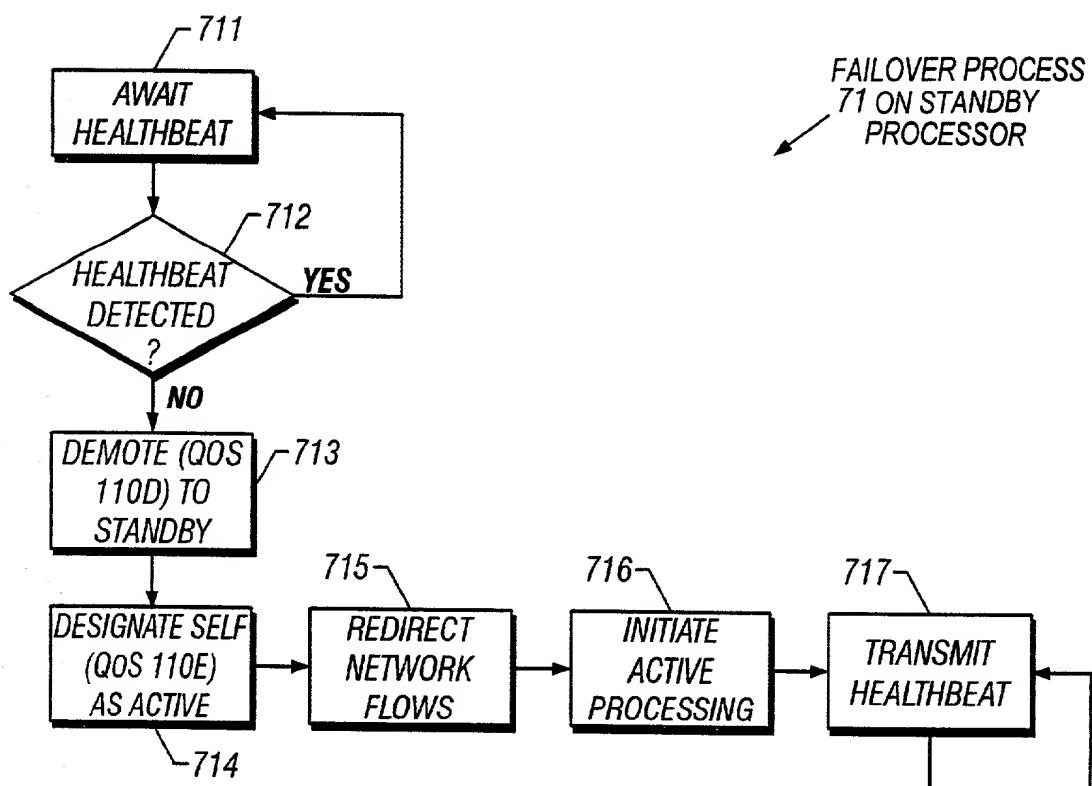


FIG. 8B

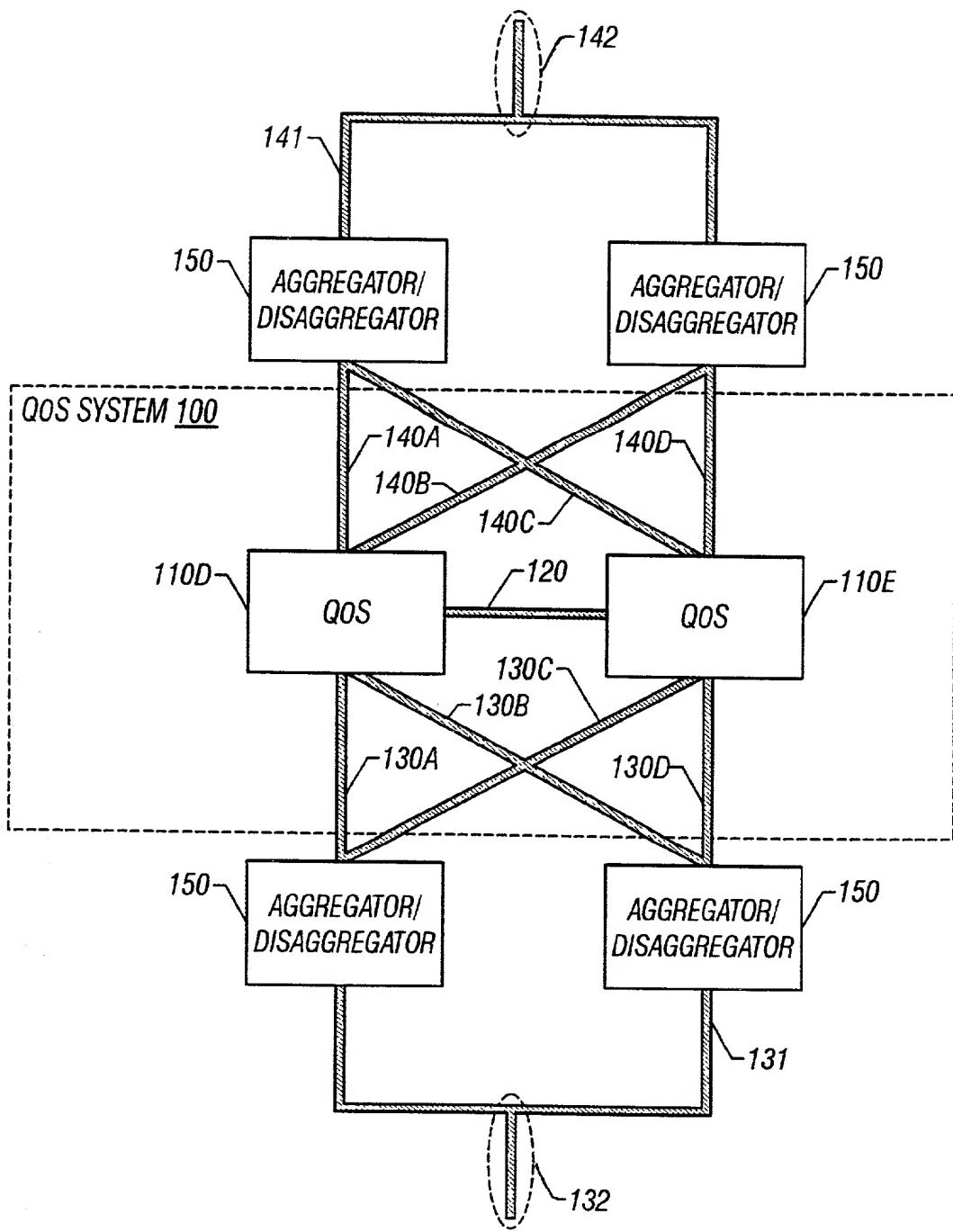


FIG. 9